

# Foreign Patent Abstracts Search Results

## Strategy 2

09/79/312

SYSTEM:OS - DIALOG OneSearch

File 350:Derwent WPIX 1963-2001/UD,UM &UP=200127

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\*File 350: Price changes as of 1/1/01. Please see HELP RATES 350.

72 Updates in 2001. Please see HELP NEWS 350 for details.

File 347:JAPIO OCT 1976-2001/JAN(UPDATED 010507)

(c) 2001 JPO & JAPIO

\*File 347: JAPIO data problems with year 2000 records are now fixed.

Alerts have been run. See HELP NEWS 347 for details.

File 344:CHINESE PATENTS ABS APR 1985-2001/May

(c) 2001 EUROPEAN PATENT OFFICE

File 371:French Patents 1961-2001/BOPI 200119

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Set	Items	Description
S1	23063	MC=T06-A04B1 OR SEQUENC?(4N)CONTROL?
S2	435539	(COMMAND? OR CONTROL?) (4N)CIRCUIT? OR MC=(W04-M01D OR X22-- H01 OR V05-F05E5A OR S06-A14 OR X24-C01 OR W03-A08A4C OR T07-- C01 OR T04-F01A OR W04-V07C OR W02-J03A)
S3	3966	S1 AND S2
S4	38069	(CONFIGUR? OR ARRANG?) (3N) (CONTROL? OR SEQUENC?)
S5	136	S3 AND S4
S6	577	(TIME? OR CLOCK?) (2N) BOUNDAR?
S7	1	S5 AND S6
S8	1	PN=EP 974881
S9	0	S7 NOT S8
S10	33	S5 AND (SUBSYSTEM? OR SUB()SYSTEM? OR COMPONENT? OR MODUL?)
S11	11	S10 AND (TIME? OR CLOCK?)
S12	62	S5 AND (TIME? OR CLOCK?)
S13	9	S12 AND (EXECUT? OR INSTRUCTION? OR ACTIVIT?)
S14	5	S10 AND INSTRUCTION?
S15	12	S13 OR S14
S16	21	S15 OR S11
S17	20143	MRI OR MAGNETIC(W) RESONANCE OR (ANGIOGRAPH? OR CARDIOANGIO- GRAPH?) (2N)NUCLEAR OR NMR OR MAGNETORESONANCE OR PMR OR PROTO- N(W)MAGNETIC(W) RESONANCE OR MR() (IMAGE? OR IMAGING)
S18	3666	MC=(S01-E02A2 OR S03-E07A OR S01-E02A8A OR S01-E02A1 OR S0- 3-E07C OR S05-D02B1)
S19	68019	S17 OR S18 OR IMAGING?
S20	2	S19 AND S16
S21	6	S19 AND S5
S22	0	S21 NOT S2

21/9/1 (Item 1 from file: 350)  
DIALOG(R)File 350:Derwent WPIX  
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012956428 \*\*Image available\*\*  
WPI Acc No: 2000-128278/200012  
XRPX Acc No: N00-096693

\*\*\*Configuring\*\*\* a \*\*\*control\*\*\* \*\*\*sequence\*\*\* in an \*\*\*imaging\*\*\*  
system e.g. medical \*\*\*MRI\*\*\* system  
Patent Assignee: GENERAL ELECTRIC CO (GENE )  
Inventor: LICATO P E; MCKINNON G C; PLOETZ L E; TAN Q  
Number of Countries: 026 Number of Patents: 002  
Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week
EP 974881	A2	20000126	EP 99305592	A	19990714	200012 B
JP 2000060822	A	20000229	JP 99206978	A	19990722	200022

Priority Applications (No Type Date): US 98120920 A 19980722

Patent Details:

Patent No	Kind	Lan Pg	Main IPC	Filing Notes
EP 974881	A2	E 16	G05B-019/042	

Designated States (Regional): AL AT BE CH CY DE DK ES FI FR GB GR IE IT  
LI LT LU LV MC MK NL PT RO SE SI

JP 2000060822 A 12 A61B-005/055

Abstract (Basic): EP 974881 A2

NOVELTY - The \*\*\*control\*\*\* \*\*\*sequence\*\*\* \*\*\*configuration\*\*\*  
involves an \*\*\*imaging\*\*\* system including independently controllable  
subsystems (56-64) for e.g. pulse and gradient field generation, and  
\*\*\*control\*\*\* \*\*\*circuitry\*\*\* for \*\*\*commanding\*\*\* activities of the  
subsystems. The method includes the steps of:

(i) defining a first logical component module (118) including  
instructions for executing a first activity (68-76) of at least one  
subsystem (56-60) and a first time boundary (128) for execution of the  
first activity;

(ii) defining a second logical component module (120) including  
instructions for executing a second activity (80) of at least one  
subsystem (64) and a second time boundary for execution of the second  
activity;

(iii) assembling the first and second logical component modules  
into a \*\*\*control\*\*\* \*\*\*sequence\*\*\*; and

(iv) storing the \*\*\*control\*\*\* \*\*\*sequence\*\*\* in a memory  
\*\*\*circuit\*\*\*.

DETAILED DESCRIPTION - The controllable subsystems are for general  
control, anchor and RF pulses, and for XYZ gradient fields.

USE - For an \*\*\*imaging\*\*\* system e.g. a medical \*\*\*magnetic\*\*\*  
\*\*\*resonance\*\*\* \*\*\*imaging\*\*\* (\*\*MRI\*\*) system.

ADVANTAGE - Coordinates and optimizes pulse sequences and other  
activities such as gradient fields on multiple system axes in a manner  
which respects the physics of the \*\*\*imaging\*\*\* process and avoids  
unwanted interactions between the multiple axes.

pp; 16 DwgNo 1/10

Title Terms: CONTROL; SEQUENCE; IMAGE; SYSTEM; MEDICAL; \*\*\*MRI\*\*\*; SYSTEM

Derwent Class: S01; S03; S05; T06

International Patent Class (Main): A61B-005/055; G05B-019/042

International Patent Class (Additional): G01R-033/48; G01R-033/54;

G06F-019/00; G06T-001/00

File Segment: EPI

*Parent  
application  
equivalent*

Manual Codes (EPI/S-X): \*\*\*S01-E02A2\*\*\*; \*\*\*S03-E07A\*\*\*; \*\*\*S05-D02B1\*\*\*;  
\*\*\*T06-A04B1\*\*\*

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21/9/2 (Item 2 from file: 350)  
DIALOG(R)File 350:Derwent WPIX  
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012845319 \*\*Image available\*\*  
WPI Acc No: 2000-017151/200002  
XRPX Acc No: N00-013569

7 Pulse \*\*\*sequence\*\*\* \*\*\*control\*\*\* \*\*\*arrangement\*\*\* for \*\*\*magnetic\*\*\*  
\*\*\*resonance\*\*\* \*\*\*imaging\*\*\* apparatus - controls supply of clock signal  
to MPU either from VCO or from standard clock of transmission system,  
depending on locking condition of PLL

Patent Assignee: HITACHI MEDICAL CORP (HITR )  
Number of Countries: 001 Number of Patents: 001  
Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week
JP 11290292	A	19991026	JP 98112863	A	19980409	200002 B

Priority Applications (No Type Date): JP 98112863 A 19980409

Patent Details:

Patent No	Kind	Lan Pg	Main IPC	Filing Notes
JP 11290292	A	7	A61B-005/055	

Abstract (Basic): JP 11290292 A

NOVELTY - The output of VCO and standard clock signal is fed to phase synchronization circuit. When there is no supply of clock from transmission system (4) to PLL (35), synchronous clock signal selector (31) selects signal from measurement group of \*\*\*NMR\*\*\* and supplies to MPU (34) and when there is supply of clock, voltage of VCO is \*\*\*controlled\*\*\* so that PLL \*\*\*circuit\*\*\* receives standard clock from transmission system (4). DETAILED DESCRIPTION - The standard clock signal and clock signal generated by VCO are of identical frequency. The frequency divider (32) divides the output signal of VCO (36) and a frequency multiplier (33) generates clock signal whose frequency is integral multiple of output of VCO.

USE - In \*\*\*magnetic\*\*\* \*\*\*resonance\*\*\* \*\*\*imaging\*\*\* apparatus for obtaining tomogram from desired portion of human body.

ADVANTAGE - As the supply of clock signals is not concerned with ON/OFF of transmission system power supply, it is not necessary to load software to the degree of ON/OFF of the system and the signals can be supplied with stability to the MPU which inturn stabilizes the operation of MPU. By performing synchronization, the disturbance of the phase of a clock is eliminated. DESCRIPTION OF DRAWING(S) - The figure shows the block diagram of clock generating part of the sequencer. (4) Transmission system; (31) Synchronous clock signal selector; (32) Frequency divider; (33) Frequency multiplier; (34) MPU; (35) PLL; (36) Voltage controlled oscillator.

Dwg.1/5

Title Terms: PULSE; SEQUENCE; CONTROL; ARRANGE; MAGNETIC; RESONANCE; IMAGE;  
APPARATUS; CONTROL; SUPPLY; CLOCK; SIGNAL; MPU; VCO; STANDARD; CLOCK;  
TRANSMISSION; SYSTEM; DEPEND; LOCK; CONDITION; PLL

Derwent Class: P31; S01

International Patent Class (Main): A61B-005/055

International Patent Class (Additional): G01R-033/36; G01R-033/48  
File Segment: EPI; EngPI  
Manual Codes (EPI/S-X): S01-E02A

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21/9/3 (Item 3 from file: 350)  
DIALOG(R) File 350:Derwent WPIX  
(c) 2001 Derwent Info Ltd. All rts. reserv.

010251817 \*\*Image available\*\*  
WPI Acc No: 1995-153072/199520  
XRPX Acc No: N95-120509

Electronic still camera - has sequential \*\*\*control\*\*\* \*\*\*circuit\*\*\*  
which changes predetermined data recorded in recording medium when  
recording of new image data begins

Patent Assignee: NIKON CORP (NIKR )  
Inventor: KAWAMURA K; SUZUKI M  
Number of Countries: 002 Number of Patents: 002  
Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week
JP 7079403	A	19950320	JP 93179852	A	19930625	199520 B
US 5585845	A	19961217	US 94265816	A	19940627	199705
			US 95567743	A	19951205	

Priority Applications (No Type Date): JP 93179852 A 19930625

Patent Details:

Patent No	Kind	Lan Pg	Main IPC	Filing Notes
JP 7079403	A	10	H04N-005/907	
US 5585845	A	11	H04N-005/76	Cont of application US 94265816

Abstract (Basic): JP 7079403 A

The electronic still camera has the image pickup processing part which has the photographed object's image recorded as image data in a recording medium (6). This image data recorded in the recording medium is \*\*\*controlled\*\*\* by a sequential \*\*\*control\*\*\* \*\*\*circuit\*\*\* provided at the recording end. It changes the predetermined data recorded in the recording medium when the recording of new image data begins.

ADVANTAGE - Restores or deletes invalid data in memory card of electronic still camera. Enables effective use of memory card. Enables to detect whether recording operation is interrupted.

Dwg.1/6

Abstract (Equivalent): US 5585845 A

An electronic still camera comprising:  
an \*\*\*imaging\*\*\* unit to produce image data corresponding to an image of a subject to be imaged;  
a recording medium to record said image data; and  
a \*\*\*sequence\*\*\* \*\*\*controller\*\*\* to \*\*\*control\*\*\* recording of said image data on said recording medium, said \*\*\*sequence\*\*\* \*\*\*controller\*\*\* \*\*\*configured\*\*\* to modify specified data to be recorded at the beginning of the recording of said image data on said recording medium, said specified data indicating the validity of said image data.

Dwg.1/2

Title Terms: ELECTRONIC; STILL; CAMERA; SEQUENCE; CONTROL; CIRCUIT; CHANGE; PREDETERMINED; DATA; RECORD; RECORD; MEDIUM; RECORD; NEW; IMAGE; DATA; BEGIN

Derwent Class: W04  
International Patent Class (Main): H04N-005/76; H04N-005/907  
International Patent Class (Additional): G11B-019/02; H04N-005/91  
File Segment: EPI  
Manual Codes (EPI/S-X): W04-M01B1C; W04-M01D6; W04-P01C5

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21/9/4 (Item 4 from file: 350)  
DIALOG(R)File 350:Derwent WPIX  
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008891268 \*\*Image available\*\*  
WPI Acc No: 1992-018537/199203  
XRPX Acc No: N92-014081

Microcode \*\*\*sequencer\*\*\* \*\*\*controller\*\*\* for \*\*\*NMR\*\*\* \*\*\*imaging\*\*\* -  
has level sensitive external acting to permit sequences and WAIT  
instructions to be executed when external gating is desired or not  
Patent Assignee: UNIV CALIFORNIA (REGC )  
Inventor: HOENNINGER J C  
Number of Countries: 003 Number of Patents: 005  
Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week
GB 2245992	A	19920115	GB 9017816	A	19900814	199203 B
DE 4036968	A	19920116	DE 4036968	A	19901120	199204
GB 2245992	B	19940202	GB 9017816	A	19900814	199404
US 5291610	A	19940301	US 90551798	A	19900712	199409
US 5481744	A	19960102	US 90551798	A	19900712	199607
			US 93147552	A	19931105	

Priority Applications (No Type Date): US 90551798 A 19900712; US 93147552 A  
19931105

Patent Details:

Patent No	Kind	Lan	Pg	Main IPC	Filing Notes
US 5291610	A	17		G06F-009/00	
US 5481744	A	17		G06F-009/00	Cont of application US 90551798 Cont of patent US 5291610
GB 2245992	B			G05B-015/00	

Abstract (Basic): GB 2245992 A

The \*\*\*sequencer\*\*\* \*\*\*controller\*\*\* includes a level-sensitive external gating \*\*\*arrangement\*\*\*. When a \*\*\*sequencer\*\*\* microcode Wait instruction is executed, the gating arrangement operates differently depending on the level of the signal (176) existing at the external gating input. If the external gating signal level is at one level, the gating \*\*\*arrangement\*\*\* causes the \*\*\*sequencer\*\*\* to wait until the external gating input changes level - thus permitting an external gating event (e.g., closure of a breath switch or the like) to interact with and control the timing of the \*\*\*NMR\*\*\* sequence.

If the external gating signal is at a different level when the Wait instruction is first executed, however, the sequencer does not 'wait' at all but instead ignores the Wait instruction and goes to the next sequencer instruction. Such level sensitive external gating permits microcode sequences including Wait instructions to be executed when external gating is desired and also when external gating is not desired. Thus a separate code for each desire is unnecessary.

USE/ADVANTAGE - Medical use. Sensitive to level (rather than

transitions) of external gating signal. (48pp Dwg.No.4/7  
Abstract (Equivalent): GB 2245992 B

A method of operating a microcoded \*\*\*control\*\*\* \*\*\*sequencer\*\*\*  
\*\*\*controlling\*\*\* a nuclear \*\*\*magnetic\*\*\* \*\*\*resonance\*\*\*  
\*\*\*imaging\*\*\* system, said sequencer having provided thereto a  
microcode instruction sequence including at least one microcode  
instruction encoding a WAIT command, said sequencer receiving an  
external gating signal, said method comprising the following steps: a)  
generating clock pulses; b) determining whether a provided microcode  
instruction encodes said WAIT command; c) testing a level of said  
external gating signal, d) if said testing step c) reveals that said  
external gating signal level is at a first predetermined level,  
ignoring a WAIT command determined by said determining step b), and e)  
waiting until said external gating signal level changes and a generated  
clock pulse occurs before executing a further microcode instruction if  
said determining step b) determines that said provided microwave  
instruction encodes said WAIT command and said testing step c) reveals  
said external gating signal is a level that is different from said  
first predetermined level.

Abstract (Equivalent): US 5481744 A

A nuclear \*\*\*magnetic\*\*\* \*\*\*resonance\*\*\* \*\*\*imaging\*\*\* system  
comprising:  
at least one magnetic field source that applies a magnetic field to  
a body;  
an RF transmitter for applying RF excitation pulses to said body in  
response to a first control signal;  
an RF receiver that receives, under control of a second control  
signal, nuclear \*\*\*magnetic\*\*\* \*\*\*resonance\*\*\* responses generated by  
said body in response to said RF excitation pulses;  
\*\*\*imaging\*\*\* means connected to said RF receiving means for  
generating an image based on said received responses;  
a sequencer for repetitively generating said first and second  
control signals in real time under control of a program specifying a  
time sequence of said RF excitation pulses and said response  
receptions, said program defining at least one WAIT command; and  
an external gating circuit connected to said sequencer, said  
external gating circuit connected to receive an external gate signal,  
said external gating circuit including a testing circuit for testing  
said external gate signal to determine whether said signal is at a  
predetermined level, said external gating \*\*\*circuit\*\*\* disabling said  
WAIT \*\*\*command\*\*\* and allowing said sequencer to proceed in said  
repetitive generating of said first and second \*\*\*control\*\*\* signals  
when said testing \*\*\*circuit\*\*\* determines said signal is at said  
predetermined level, said external gating \*\*\*circuit\*\*\*  
\*\*\*controlling\*\*\* said \*\*\*sequencer\*\*\* to wait in response to the WAIT  
command until said external gate signal changes level when said testing  
circuit determines said external gate signal is at a level different  
from said Predetermined level.

Dwg.2/7

US 5291610 A

The \*\*\*sequencer\*\*\* \*\*\*controller\*\*\* for nuclear \*\*\*magnetic\*\*\*  
\*\*\*resonance\*\*\* \*\*\*imaging\*\*\* includes a level-sensitive external  
gating \*\*\*arrangement\*\*\*. When a \*\*\*sequencer\*\*\* microcode WAIT  
instruction is executed, the gating arrangement operates differently  
depending on the level of the signal existing at the external gating  
input. If the external gating signal level is at one level, the gating  
\*\*\*arrangement\*\*\* causes the \*\*\*sequencer\*\*\* to wait until the external

gating input changes level-thus permitting an external gating event (e.g., closure of a breath switch or the like) to interact with and control the timing of the \*\*\*NMR\*\*\* sequence.

If the external gating signal is at a different level when the WAIT instruction is first executed, however, the sequencer does not 'wait' at all but instead ignores the WAIT instruction and goes to the next sequencer state.

USE/ADVANTAGE - Pulse programmer. Level sensitive external gating permits microcode sequences including WAIT instructions to be executed when external gating is desired and also when external gating is not desired, thus reducing amount of code that needs to be maintained.

Dwg.3/7

Title Terms: SEQUENCE; CONTROL; \*\*\*NMR\*\*\*; IMAGE; LEVEL; SENSITIVE; EXTERNAL; ACT; PERMIT; SEQUENCE; WAIT; INSTRUCTION; EXECUTE; EXTERNAL; GATE

Derwent Class: S01; S03; S05; T01

International Patent Class (Main): G05B-015/00

International Patent Class (Additional): G01N-024/08; G01R-033/28;

G05B-019/18; G06F-009/44

File Segment: EPI

Manual Codes (EPI/S-X): S01-E02A; S03-E07; S05-D02B; T01-F01

21/9/5 (Item 5 from file: 350)  
DIALOG(R)File 350:Derwent WPIX  
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007822356 \*\*Image available\*\*

WPI Acc No: 1989-087468/198912

XRPX Acc No: N89-066716

\*\*\*Imaging\*\*\* appts. with interface for computer system - has computer  
\*\*\*arranged\*\*\* to generate \*\*\*control\*\*\* signals which are used to  
\*\*\*control\*\*\* \*\*\*sequence\*\*\* of signals applied to sensor

Patent Assignee: ENGLISH ELECTRIC VALVE CO LTD (ENGE )

Inventor: MORCOM C J

Number of Countries: 013 Number of Patents: 004

Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week
EP 308075	A	19890322	EP 88307664	A	19880818	198912 B
GB 2210228	A	19890601	GB 8721977	A	19870918	198922
US 4896215	A	19900123	US 88245111	A	19880915	199011
GB 2210228	B	19911113				199146

Priority Applications (No Type Date): GB 8721977 A 19870918

Cited Patents: A3...9022; EP 195270; No-SR.Pub; US 4570184

Patent Details:

Patent No	Kind	Lan	Pg	Main IPC	Filing Notes
EP 308075	A	E	10		

Designated States (Regional): AT BE CH DE ES FR GB IT LI LU NL SE

US 4896215 A 7

Abstract (Basic): EP 308075 A

The \*\*\*imaging\*\*\* apparatus comprises an image sensor, a device arranged to receive an output from the image sensor and an interface between them. The interface is arranged to apply a number of signals to the image sensor to produce the output. The sequence in which signals

are applied is determined by control signals applied to the interface. Usually the device will be a computer or a frame store of a computer. by barying the sequence in which the number of signals are applied to the image sensor by the interface, a number of different types of sensor may be used.

ADVANTAGE - Reduced picture blur

Abstract (Equivalent): GB 2210228 B

\*\*\*Imaging\*\*\* apparatus comprising a solid state image sensor, a device arranged to receive an output from the image sensor and an interface connecting them, the interface being arranged to apply a plurality of signals to the image sensor, each signal causing to be performed a respective function within the sensor to transfer image data from sensing elements of the sensor to an output of the sensor, and means for applying control signals to be the interface to cause to be generated the said plurality of signals such that the sequence in which the functions are performed is determined by the controls signals, enabling the interface to be used with more than one type of image sensor.

Abstract (Equivalent): US 4896215 A

The image sensor (1) is connected to a computer, or frame store, (7) via an interface (8). The interface has three control line inputs (9,10,and11) which are arranged to carry the field control signal, the line readout control signal and the picture element control signal respectively. The interface is such that signals applied on lines \*\*\*control\*\*\* the \*\*\*sequence\*\*\* and timing of signals applied to the image sensor to control it. The interface comprises a logic section (12) and video processing circuitry (13).

The video processing image sensor and convert it into a video signal which is then transmitted to the computer

Title Terms: IMAGE; APPARATUS; INTERFACE; COMPUTER; SYSTEM; COMPUTER; ARRANGE; GENERATE; CONTROL; SIGNAL; CONTROL; SEQUENCE; SIGNAL; APPLY; SENSE

Derwent Class: T01; W04

International Patent Class (Additional): H04N-003/15; H04N-005/23

File Segment: EPI

Manual Codes (EPI/S-X): T01-C; T01-J10A; W04-M01B; \*\*\*W04-M01D\*\*\*; W04-M09

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21/9/6 (Item 6 from file: 350)  
DIALOG(R)File 350:Derwent WPIX  
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004331860

WPI Acc No: 1985-158738/198526

XRPX Acc No: N85-119731

Distance measuring device for photographic camera - uses storage type radiant ray sensor contg. photodiodes, RAM and \*\*\*sequence\*\*\*  
\*\*\*controller\*\*\*

Patent Assignee: CANON KK (CANO )

Inventor: MASUNAGA M

Number of Countries: 001 Number of Patents: 001

Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week
US 4522492	A	19850611	US 82393320	A	19820629	198526 B

Priority Applications (No Type Date): JP 81108718 A 19810710



Patent Details:

Patent No	Kind	Lan	Pg	Main IPC	Filing Notes
US 4522492	A		12		

Abstract (Basic): US 4522492 A

The distance measuring device is comprised of a system which projects infrared rays on a distant object and a light receiving optical system positioned at a prescribed base length from the infrared ray projecting system. A signal storage type sensor is provided, including an array of light sensitive elements arranged in a position which approximately coincides with the \*\*\*imaging\*\*\* plane of the light receiving optical system.

A \*\*\*control\*\*\* \*\*\*circuit\*\*\* is \*\*\*arranged\*\*\* to cause the sensor to perform a storing action under a first condition in which the infrared ray projecting system is allowed to operate and also under a second condition in which the infrared ray projection system is not allowed to operate. A digital computing device, such as a microcomputer designed to detect a difference between output signals of every light sensitive element respectively produced under the first and second conditions and detects a distance to the distant object on the basis of the results of detection of the difference between these signals.

USE - For automatic focusing control.

1/7

Title Terms: DISTANCE; MEASURE; DEVICE; PHOTOGRAPH; CAMERA; STORAGE; TYPE; RADIANT; RAY; SENSE; CONTAIN; PHOTODIODE; RAM; SEQUENCE; CONTROL

Derwent Class: S02

International Patent Class (Additional): G01C-003/10

File Segment: EPI

Manual Codes (EPI/S-X): S02-A05A; S02-B01